Name: Student University Roll No.:	Printed Pages:01
School of Engineering **First Sessional Examination, Odd Semester (AS: 2023-24)** **B. Tech: CSE3** **Year:** 3rd **Semester:** 5th	
Course Title: Microprocessor and Interfacing **Course Code: BCS 3502**	**M.M.: 30** **Time: 1 *hr***

Instructions if any: Read the question Carefully.

	SECTION 'A' **Q.N.1. Attempt all parts of the following:**	**Course Objective**	**Marks**
a)	What is memory hierarchy structure?	CO1	1
b)	Differentiate between SRAM and DRAM.	CO1	1
c)	Define flag registers.	CO2	1
d)	What do you mean by temporary register and accumulator?	CO2	1
e)	Define Status and Control Signals.	CO2	1
	SECTION 'B' **Q.N.2. Attempt any two parts of the following:**	**Course Objective**	**Marks**
a)	Explain the types of addressing modes in 8085 microprocessor.	CO1	7.5
b)	Explain the types of interrupts in microprocessor.	CO2	7.5
c)	Discuss the timing and control diagram for Opcode fetch operation in 8085 microprocessor.	CO2	7.5
d)	Differentiate between (any one) i) Microprocessor and Microcontroller. ii) CISC and RISC architecture.	CO1	7.5
	SECTION 'C' **Q.N.3. Attempt any one part of the following**	**Course Objective**	**Marks**
a)	Explain the various data transfer schemes in microprocessor.	CO1	10
b)	Explain (any one) i) Architecture of 8085 microprocessor ii) Pin diagram of 8085 microprocessor.	CO2	10
c)	Discuss the arithmetic and data transfer instructions in 8085 microprocessor.	CO2	10

Table 1: Mapping between COs and questions

(Number of COs may vary from course to course)

COs	Questions Numbers	Total Marks
CO1	1a),1b),2a),2d),3a)	27
CO2	1c),1d),1e),2b),2c),3b),3c)	38