Student Name: University Roll No.:	Printed Pages:1

School of Engineering
First Theory Sessional Examination
Odd Semester (AS: 2024-25)

B. Tech: CSE **[Year:3rd]** **[Semester :5th]**

Course Title: Microprocessor and Interfacing	Max Marks: 30
Course Code: BCS3502	Time: 1*hrs*

Instructions: 1-Mention any assumptions made.
2-Notations have usual meaning.

SECTION 'A' Q.N.1. Attempt all parts of the following:		Course Objective	Marks
a)	Explain RAM & ROM.	CO1	1
b)	Explain Cache Memory.	CO1	1
c)	Explain LDA & STA instruction with example.	CO2	1
d)	What is ALE?	CO2	1
e)	What are maskable and non-maskable interrupts in 8085 microprocessor.	CO2	1

SECTION 'B' *Q.N.2. Attempt any two parts of the following:*		Course Objective	Marks
a)	Explain the Flag register of 8085 microprocessor.	CO1	7.5
b)	Write the program to add two 8-bit numbers in 8085 microprocessors.	CO2	7.5
c)	Explain Memory and its types in detail.	CO1	7.5

SECTION 'C' Q.N.3. Attempt any one parts of the following:		Course Objective	Marks
a)	Explain the architecture of 8085 microprocessor.	CO1	10
b)	Write the features and advantages of different PENTIUM series.	CO2	10
c)	Draw the timing diagram for execution of MVI C,84H.	CO2	10

Table 1: Mapping between COs and questions

COs	Questions Numbers	Total Marks
CO1	Q1(a,b) Q2(a,c) &Q3(a)	27
CO2	Q1(c,d,e) Q2(b) &Q3(b,c)	30.5